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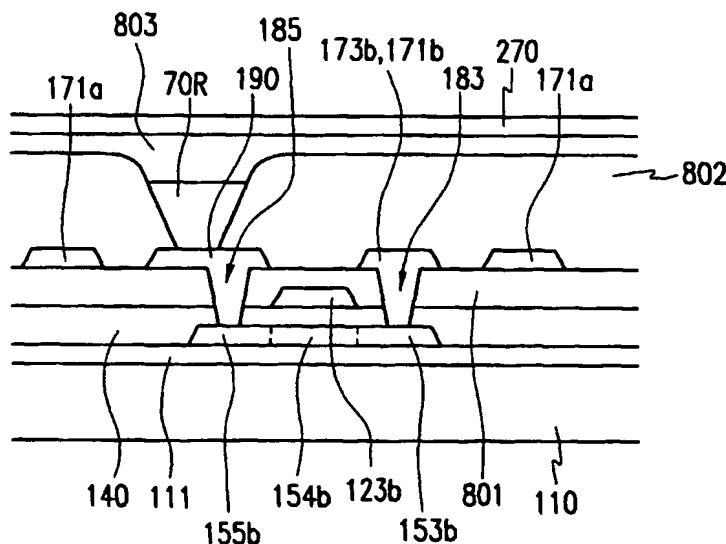
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(54) Title: ORGANIC ELECTROLUMINESCENCE DISPLAY PANEL AND MANUFACTURING METHOD THEREOF



(57) Abstract: A polysilicon layer is formed on an insulating substrate and a gate insulating layer is formed on the polysilicon layer. A gate line is formed on the gate insulating layer and an interlayer insulating film is formed on the gate line. A data line and a pixel electrode are formed on the interlayer insulating film. A partition is formed on the data line and the pixel electrode and an organic EL layer is formed on the pixel electrode in a predetermined area defined by the partition. A common electrode is formed on the organic EL layer.